METHOD AND APPARATUS TO WRITE DATA

Abstract

      Briefly, in accordance with an embodiment of the invention, a method and

5   apparatus to write data is provided. The apparatus may include a circuit to deassert a

chip select signal to latch data in a first memory after asserting a write enable signal

and prior to deasserting the write enable signal, wherein the chip select signal is

coupled to the first memory and the write enable signal is coupled to both the first

memory and a second memory.   The method may include asserting a chip select

10 signal after asserting of a write enable signal and then deasserting the chip select

signal to latch the data in a memory, wherein the deasserting of the chip select signal

occurs prior to the deasserting of the write enable signal. Other embodiments are

described and claimed.

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